

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

Corisis et. al.

Serial No.: 10/646,420

Filed: August 22, 2003

**For:** METHODS FOR DESIGNING BOND PAD REROUTING ELEMENTS FOR USE

IN STACKED SEMICONDUCTOR DEVICE ASSEMBLIES AND FOR ASSEMBLING SEMICONDUCTOR

**DEVICES** 

Confirmation No.: Unknown

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-4814.1US

## CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 1, 2003

Date

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## PRELIMINARY AMENDMENT

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Prior to examination of the above-referenced patent application on the merits, entry of the amendments as set forth herein is respectfully solicited.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing which begins on page 4 of this paper.

Remarks begin on page 9 of this paper.